

FIG. 1 A

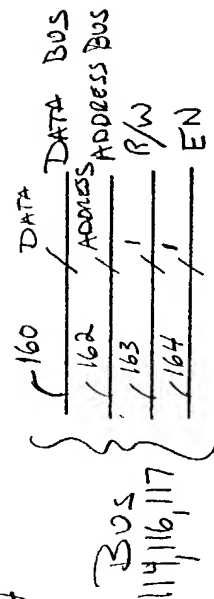


FIG. 1 B

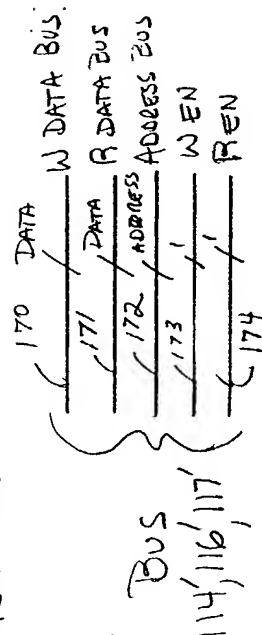
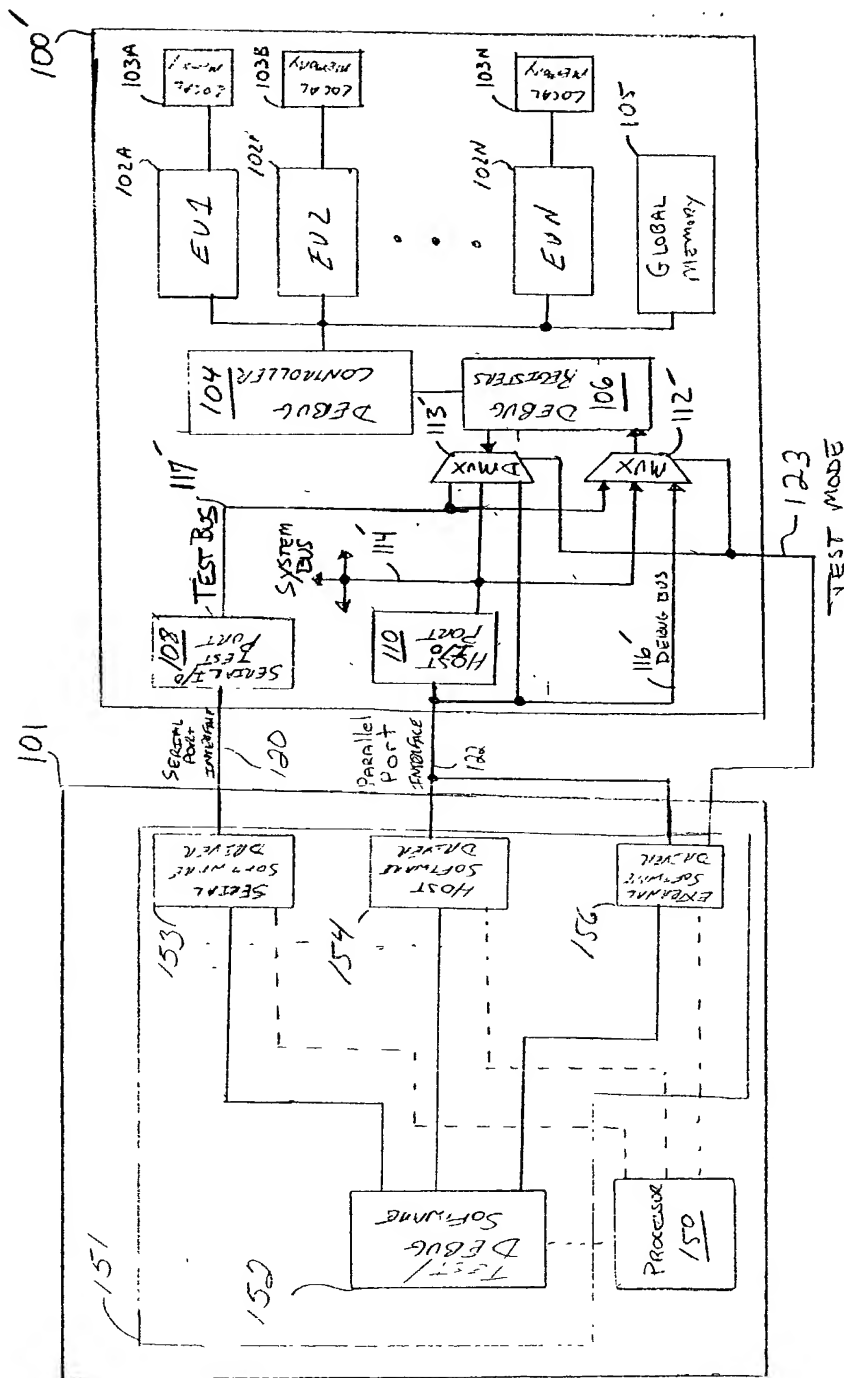


FIG. 1C

FIG. 1D

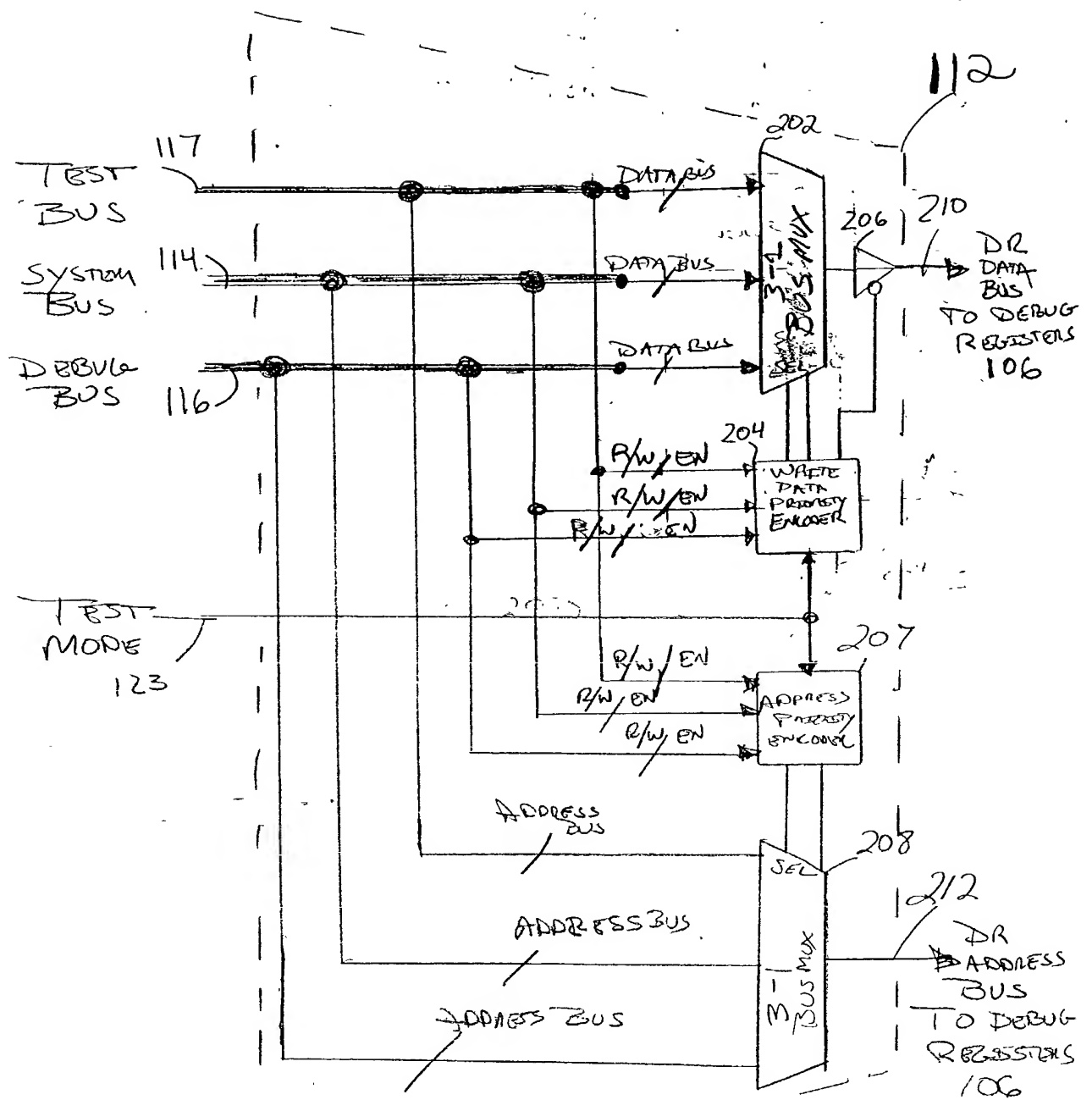


FIG. 2A

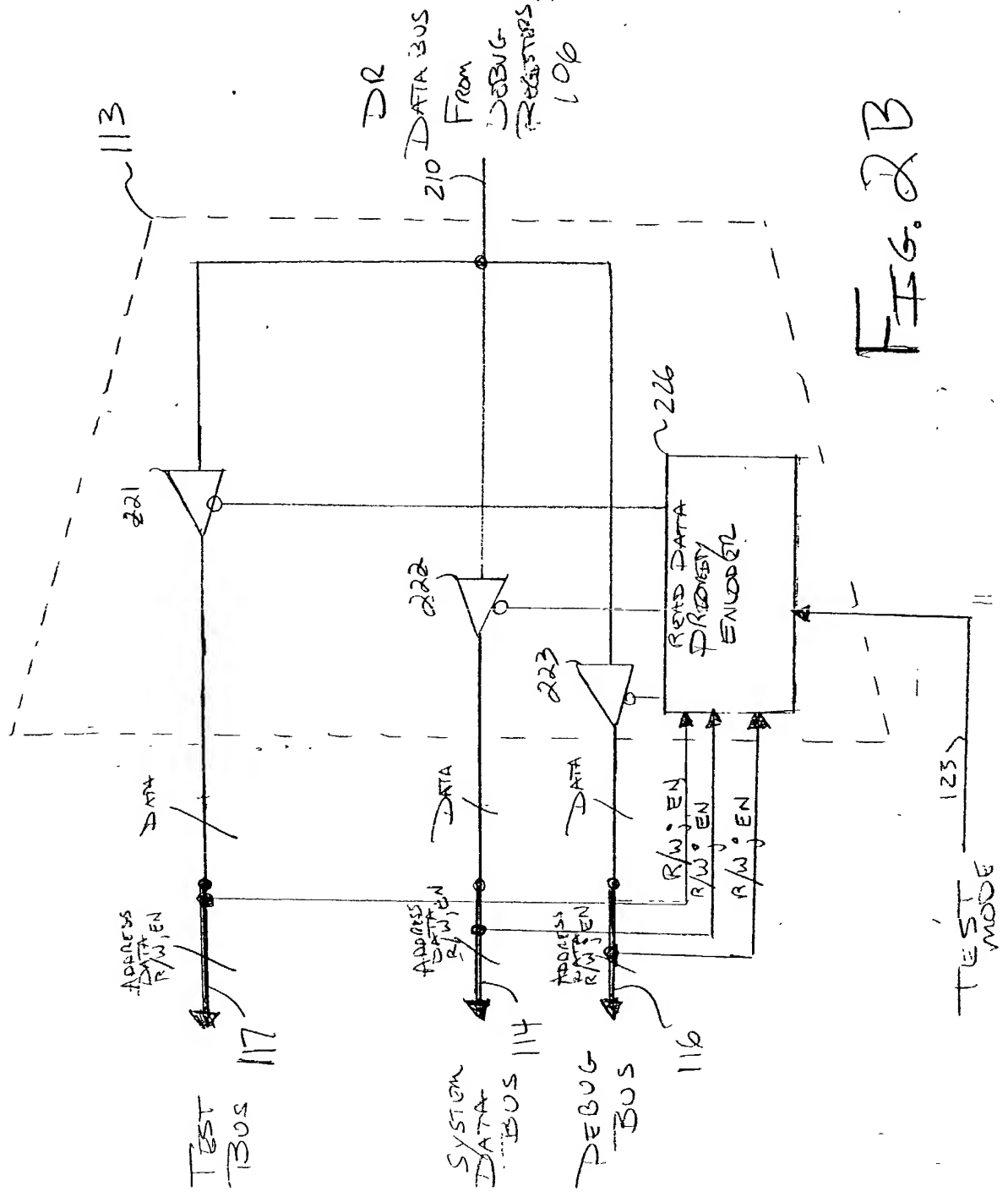
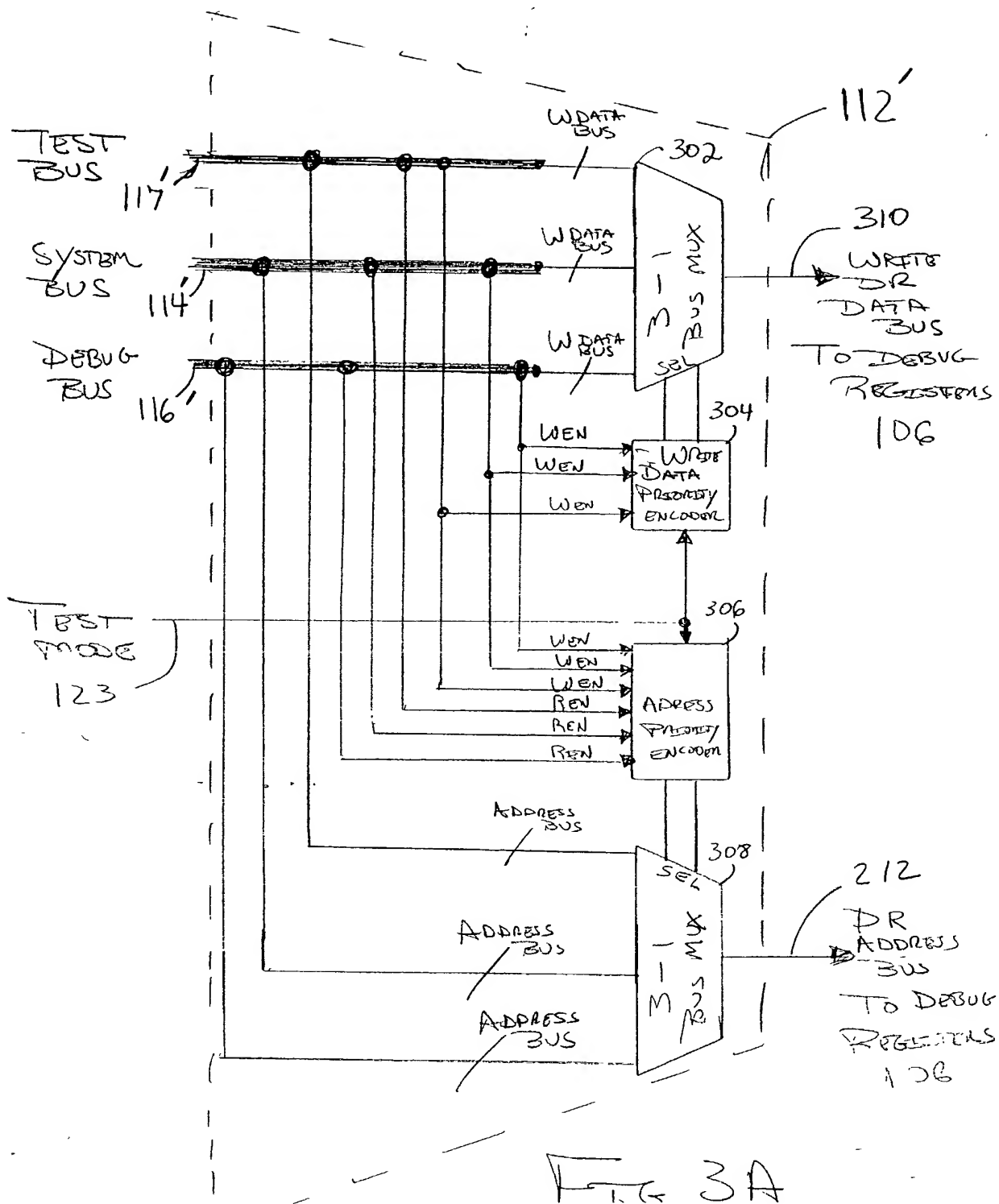


FIG. 2B



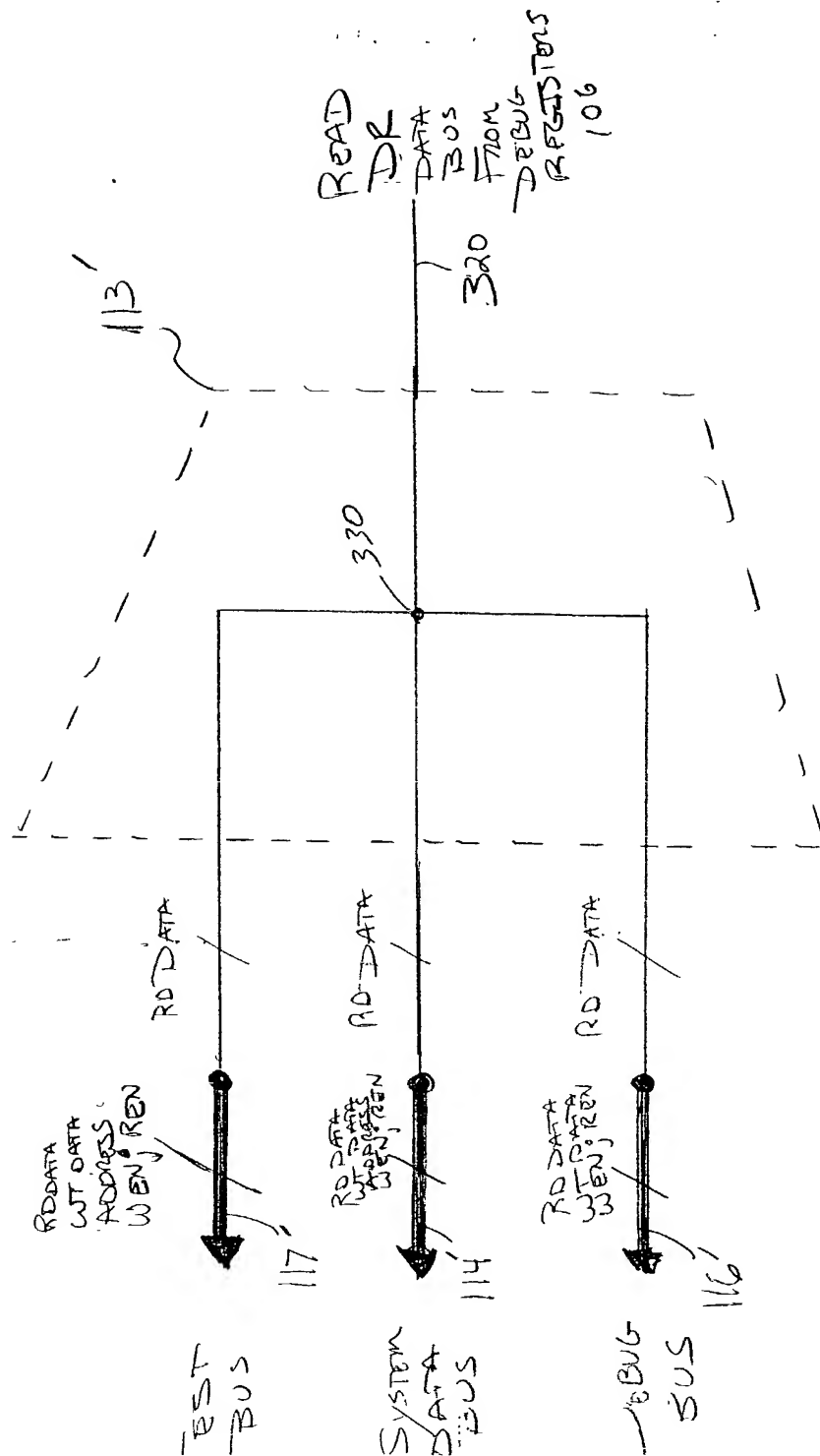


FIG. 3B

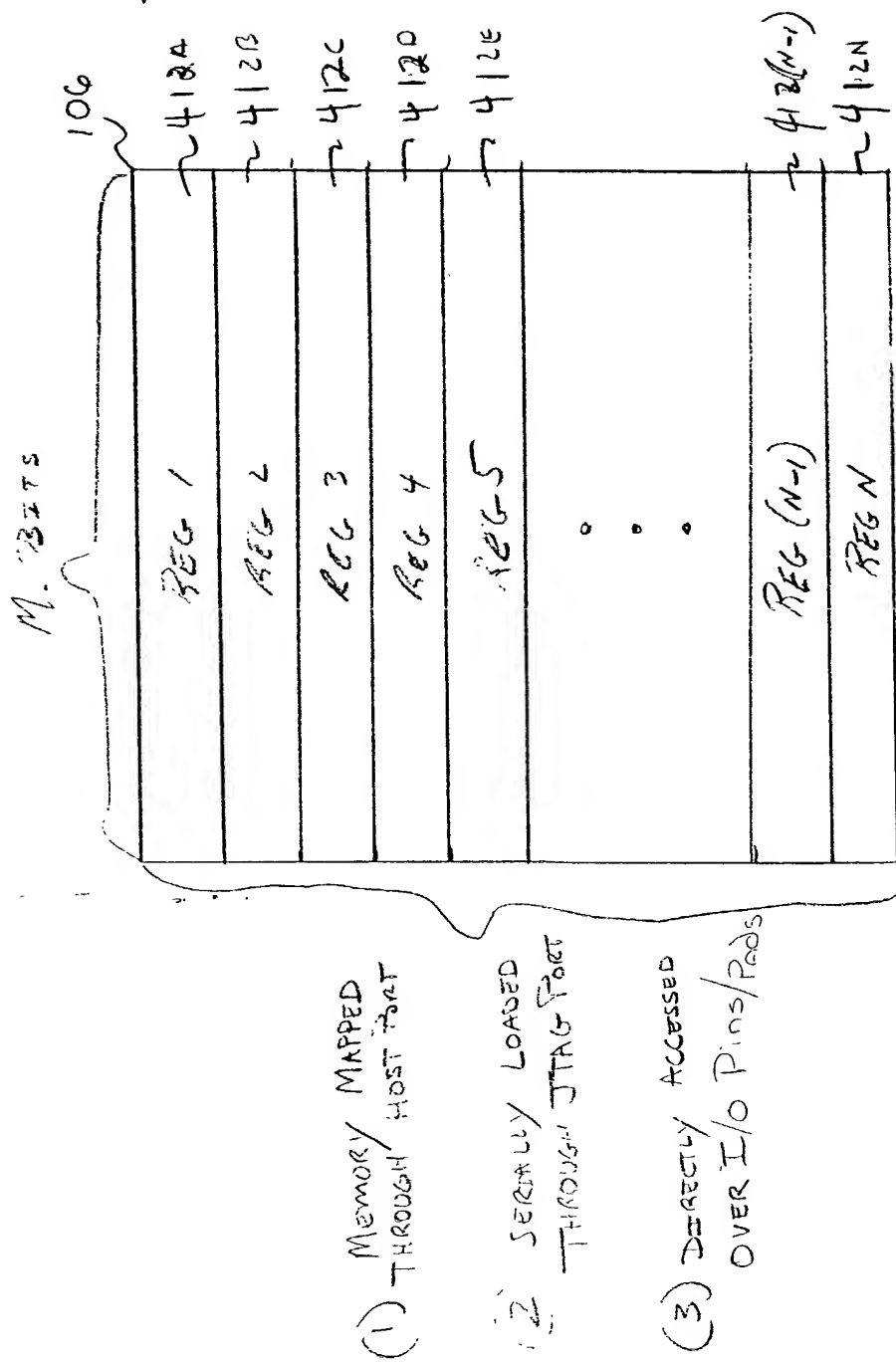


FIG. 4

SERIAL PORT INTERFACE

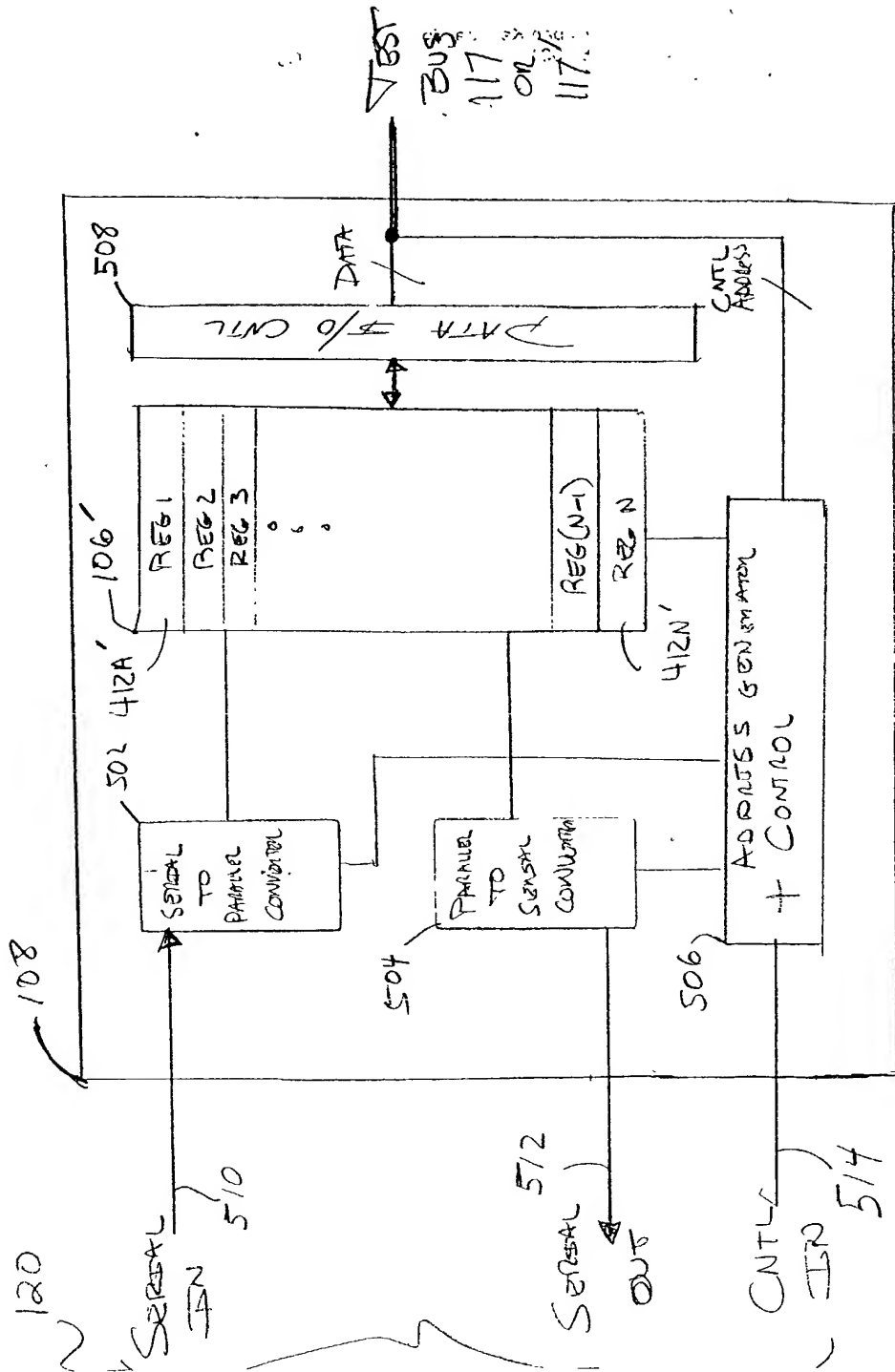


FIG. 5

